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Education

PhD : Electronics and Communication Engineering, CUSAT, 2022

Master of Technology: Opto Electronics and Laser Technology, 2005-2007

International School of Photonics, CUSAT.

Bachelor of Technology: Electronics & Communication Engineering, 1998

NSS Engineering College Palakkad, Calicut University, Kerala, India.

Professional Experience

Lecturer: College of Engineering Vadakara, 1999 -2002 Aug

Lecturer: College of Engineering Trikaripur, 2002-2008

Assistant Professor: College of Engineering Trikaripur, 2008-2011

Associate Professor: College of Engineering Trikaripur, 2011- till date

Membership to professional bodies

IEEE, ISTE

Publications

I Refereed Journals

1. **V V Mahesh, T K Shahana**, Constrained least square non uniform dynamic filter bank for delay and hardware efficient digital hearing aids, *Health and Technology*, vol.9, Issue 3, pp. 355-363, 2019, Springer, doi: 10.1007 /s 12553-018-0268-9
2. **V V Mahesh, T K Shahana**, Design and synthesis of filter bank structures based on low order constrained least square and minimum phase methods for audiogram matching in digital hearing aids, *Health and Technology* vol.11, Issue 1, pp. 153-168, Springer, 2021, doi: 10.1007/s12553-020-00496-5
3. **Jinesh Mathew, V.V. Mahesh and P. Radhakrishnan**, Comparative Performance of Two Fiber Optic Ammonia Sensors Employing Different Sensing Materials, *Sensors and Transducers Journal*, Vol. 95, Issue 8, August 2008, pp.65-71

II Book Chapter

1. **V V Mahesh, T K Shahana**, Design and synthesis of filter bank structures based on constrained least square method using hybrid computing for audiogram matching in digital hearing aids, *Sustainable Intelligent Systems, Advances in Sustainability, Science and Technology*, Springer, pp. 215-234, 2021, ISBN:978-981-334-901-8
https://doi.org/10.1007/978-981-33-4901-8_13

III Presentations in Conferences

1. **V V Mahesh, T K Shahana**, Delay and Area Efficient Sound Wave Decomposition by Nonuniform Filter Bank for Digital Hearing Aids, *SAI Intelligent Systems Conference, (IntelliSys) 2016, London, Lecture notes in Networks and Systems*, Springer, doi: 10.1007/978-3-319-56994-9_5, ISBN: 978-3-319-56994-9
 2. **V V Mahesh, T K Shahana**, Design and synthesis of Filter Banks using area and power efficient stochastic computing, *IEEE World conference on smart trends in systems, security and sustainability 2020*, London, July 2020, doi: 10.1109/WorldS450073.2020.9210403
 3. **V V Mahesh, Jinesh Mathew and P Radhakrishnan**, Design and development of a Fiber optic sensor for the detection of Amonia, *International conference on Sensors and related networks*, VIT University, India, Dec. 12-14, 2007.
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